

Department: ECE & CSE

Academic Year : 2024-2025


Class: M.Tech. - 1st Semester - Regular Practical Examinations, JANUARY 2025

**TIME TABLE FOR PRACTICAL EXAMS**

Branch & Speciliazation	LABORATORY	DATE	TIME	JNTU NOS.
ECE - VLSI & ESD	HDL Programming Lab (21VLS102)	27/01/2025	09.30 AM to 12.30 PM	24341D6801
	Term Paper (21VLS103)	28/01/2025	09.30 AM to 12.30 PM	
CSE - CSE	Data Structures and Algorithms Lab (23CSE102)	27/01/2025	9.00 AM to 12.00 Noon	24341D5801 & 23341D5808
	Term Paper (23CSE103)	28/01/2025	9.00 AM to 12.00 Noon	

Note: Any discrepancies or clashes may be brought to the notice of the Examination In-charge on or before 24-01-2025

  
ACE

  
Controller of Examinations